

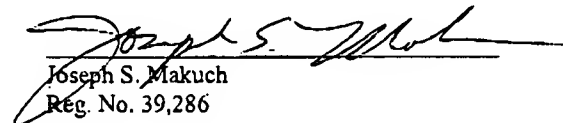
REMARKS

Claims 20 and 21 are amended in accordance with telephone calls between the Examiner and the undersigned on August 19 and 20, 2002. Claims 22 and 23 depend from claim 21, which is now in independent form.

Applicant requests reconsideration in view of the foregoing amendments and remarks. The Examiner is encouraged to telephone the undersigned at (503) 222-3613 if it appears than an interview would be helpful in advancing the case.

Respectfully submitted,


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I hereby certify that this correspondence is being transmitted by facsimile to the U.S. Patent and Trademark Office (Facsimile No. 703/308-7382) to Examiner Huan Hoang, Group Art Unit 2818 on August 20, 2002.

Signature


Laura Sabo

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VERSION WITH MARKINGS TO SHOW CHANGES MADE

In the Claims

20. (Amended) A memory system [according to claim 17 further including]
comprising:

a bus;

a stack of memory devices;

a buffer coupled between the stack of memory devices and the memory bus; and

a memory controller coupled to the bus.

21. (Amended) A memory system [according to claim 17] comprising:

a bus;

a stack of memory devices; and

a buffer coupled between the stack of memory devices and the memory bus;

wherein the stack of memory devices is mounted on a memory module.

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